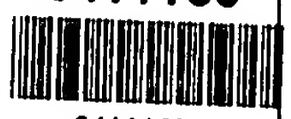
JC528 U.S. PTO
09/631697
08/03/00

Class	Subclass	ISSUE CLASSIFICATION
331	116FE	

PATENT NUMBER

6411169



6411169

U.S. **UTILITY** Patent Application

O.I.P.E.	PATENT DATE
SCANNED Q.A. AG	JUN 25 2002

APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/631697	D F	331	116.0FE	2817	Kinkead

APPLICANTS

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TITLE

[illegible] CIRCUIT, ELECTRONIC [illegible] USING THE SAME, AND SEMICONDUCTOR DEVICE, ELECTRONIC EQUIPMENT, AND TIMEPIECE USING THE SAME

PTO-2040
12/99

ISSUING CLASSIFICATION

ORIGINAL		CROSS REFERENCE(S)							
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)						
331	116FE	331	158	185					
INTERNATIONAL CLASSIFICATION									
H03	B 5/36								

☐ Continued on Issue Slip Inside File Jacket

5-29-02 [illegible] 8-3-00

☒ TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
	20	29	12	45	1

☐ The term of this patent subsequent to _____ (date) has been disclaimed.

_____ (Assistant Examiner) _____ (Date)

NOTICE OF ALLOWANCE MAILED

02-21-02

☒ The term of this patent shall not extend beyond the expiration date of U.S Patent. No. 4,046,648

ARNOLD KINKEAD
PRIMARY EXAMINER

ARNOLD KINKEAD (Primary Examiner) 2/19/02 (Date)

ISSUE FEE CM

Amount Due	Date Paid
$1,280.00	5-17-02

☐ The terminal ____months of this patent have been disclaimed.

_____ (Legal Instruments Examiner) 2/14/02 (Date)

ISSUE BATCH NUMBER

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Formal Drawing(s) set

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